



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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APR 29 2004

Technology Center 2100

Application No. : 09/821,163
Applicant : Toshiyuki Kouchi
Filed : March 28, 2001
TC/A.U. : 2133
Examiner : Dooley, Matthew C.
Title : **SEMICONDUCTOR INTEGRATED CIRCUIT**

Docket No. : 2102475-991110
Customer No. : 26379

Mail Stop Non-Fee Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

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I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as FIRST CLASS MAIL in an envelope addressed to: Mail Stop Non-Fee Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on:

April 22, 2004.



Rosa A. Caviedes

RESPONSE TO OFFICE ACTION OF JANUARY 29, 2004

Sir:

In response to the Office Action of January 29, 2004, please amend the above-identified application as follows:

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Amendments to the Drawings begin on page 7 of this paper and include both an attached replacement sheet and an annotated sheet showing changes.

Remarks/Arguments begin on page 8 of this paper.

An Appendix including amended drawing figures is attached following page 13 of this paper.